Serial Number: 09/893,527

Filing Date: June 29, 2001
Title: ARRANGEMENTS FOR AUTOMATIC RE-LEGGING OF TRANSISTORS

Assignee: Intel Corporation

## **IN THE SPECIFICATION**

Please amend the specification as follows:

Replace the equation term definitions at page 21, lines 14-18 with the following:

Dkt: P10090 (INTEL)

 $N_{new}$  - New number of legs. Device width in schematics before resizing.

 $W_{old}$  - Device width in schematics before resizing.

 $N_{\it old}$  - Old number of legs. Number of legs realizing this device in source layout.

f - Upsize factor

 $W_m$  - Maximal leg size in <u>new</u> layout <del>we wish to occur due to re-legging</del>.